



FPGA Summit 08 Conference

FMAC and the Future for FPGAs

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2008

FPGA/Analog Market Study

GUIDES FOR THE JOURNEY® | PiperJaffray

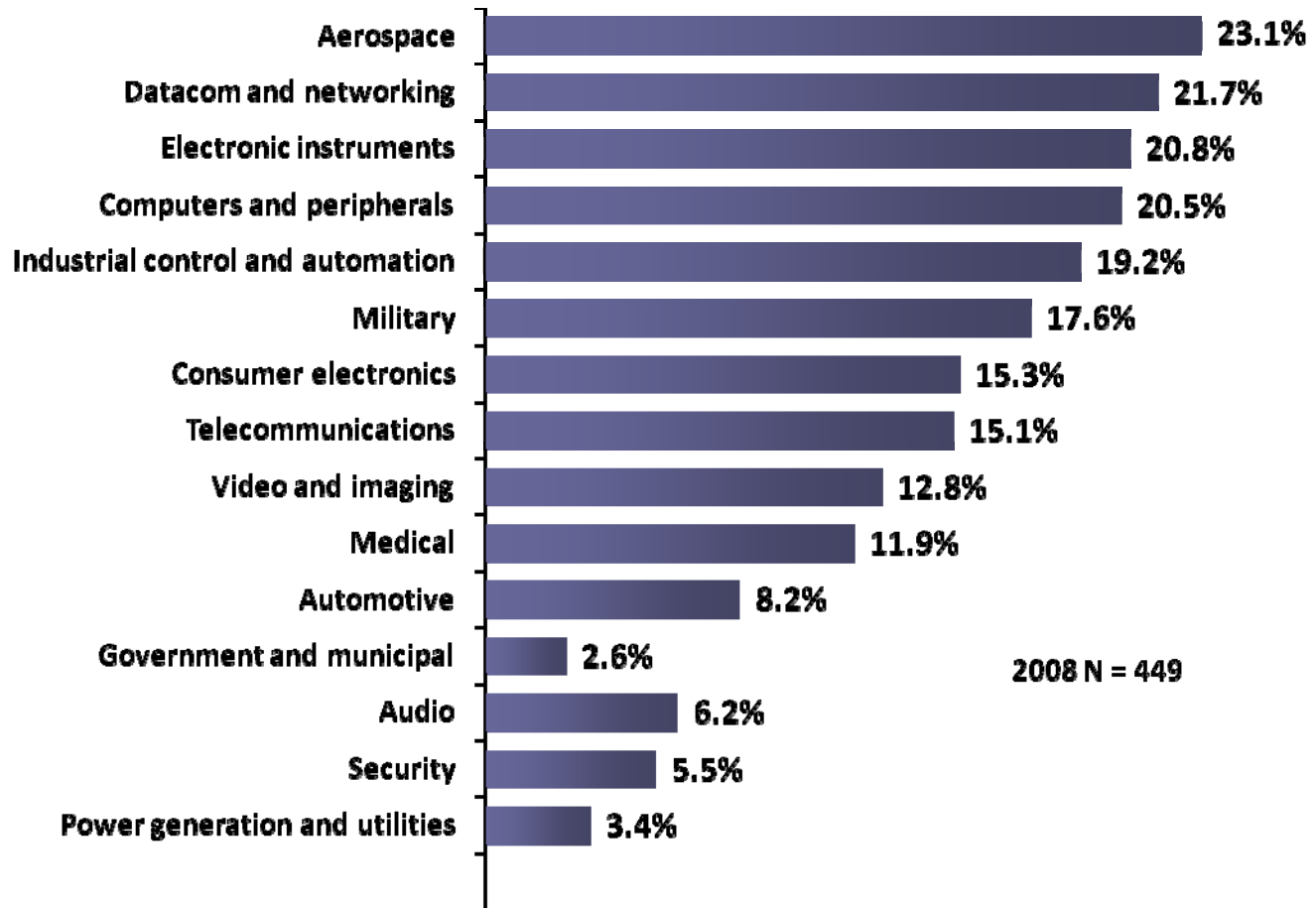


Methodology

- **Purpose**
- **Fieldwork:** Email/web survey conducted from August 14 to Sept. 10, 2008
- **Returns: 449** completed surveys, **132** specific to mil/aero users, 95% +/- 5.0% confidence overall
 - E-mail invitation of subscribers to TechInsights *EE Times*



What industry best describes your company (or project)?



Does your organization have any difficulty recruiting engineers with an adequate FPGA skill set?



Write in Questions – Talent and Training

Q. What do you do the most to prepare your designers to produce in your environment?

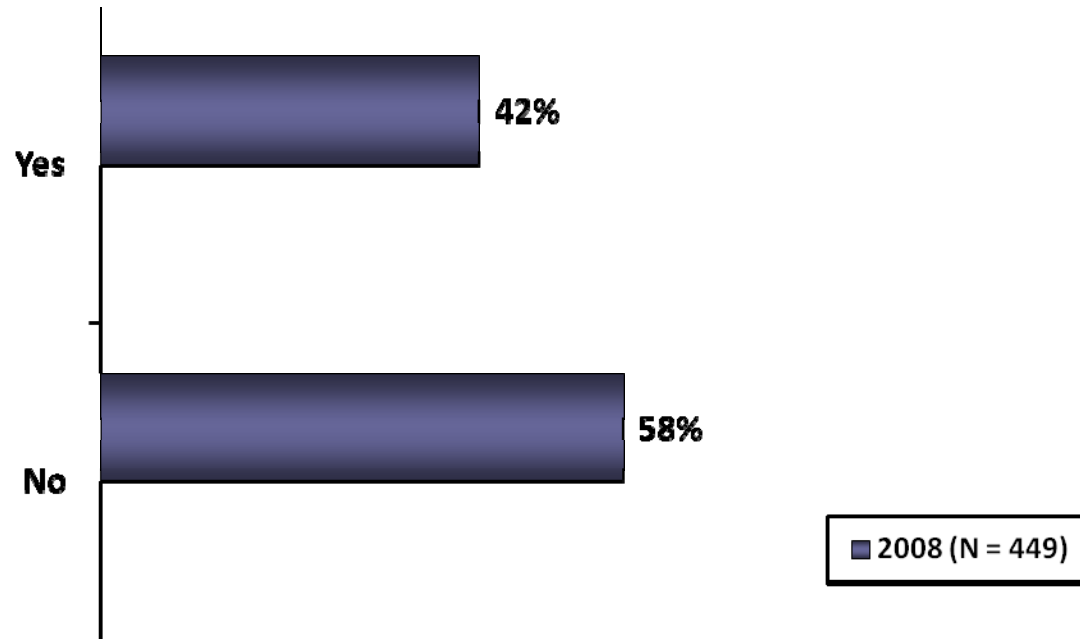
A1. In house training classes

A2. Manufacturer trainings

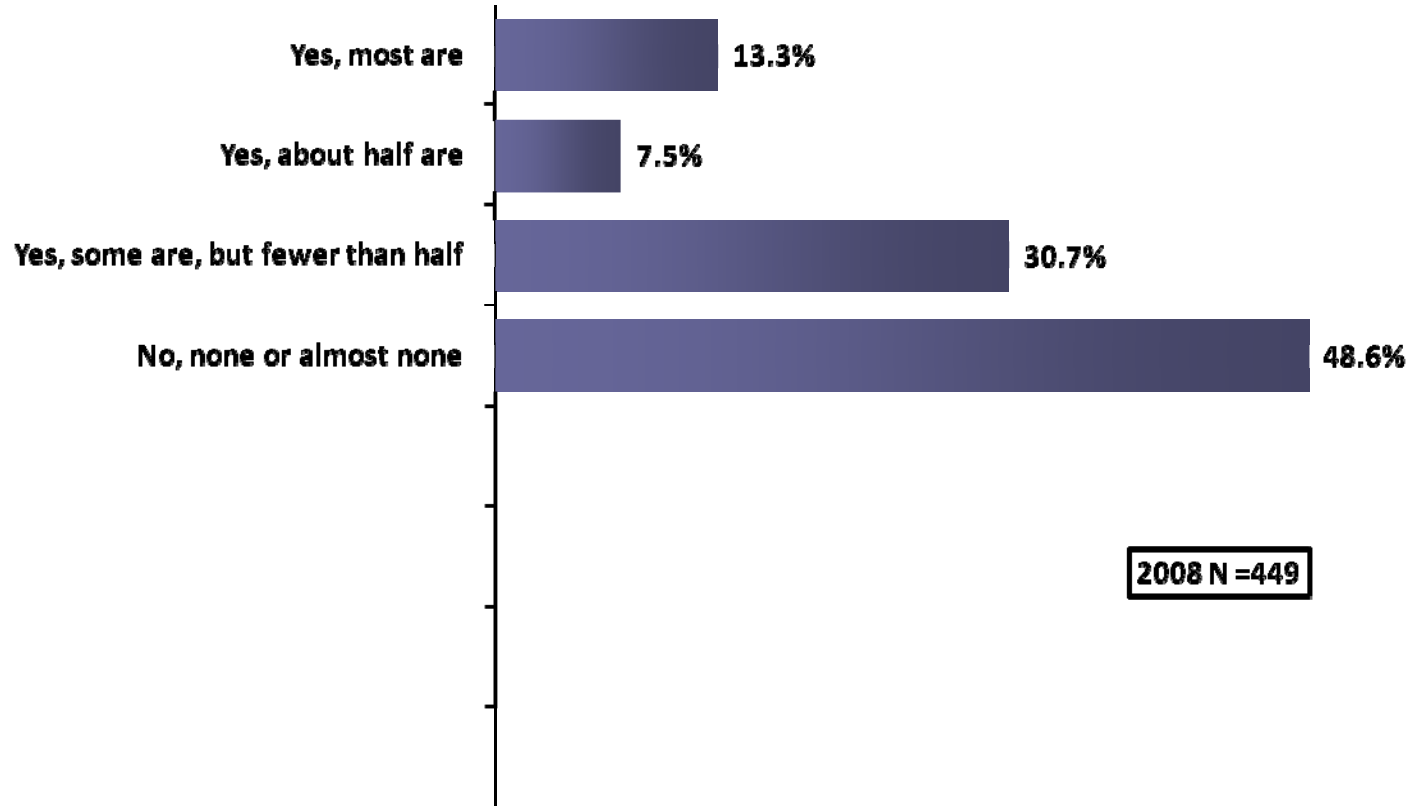
A3. Nothing, they come illprepared and they stay that way.



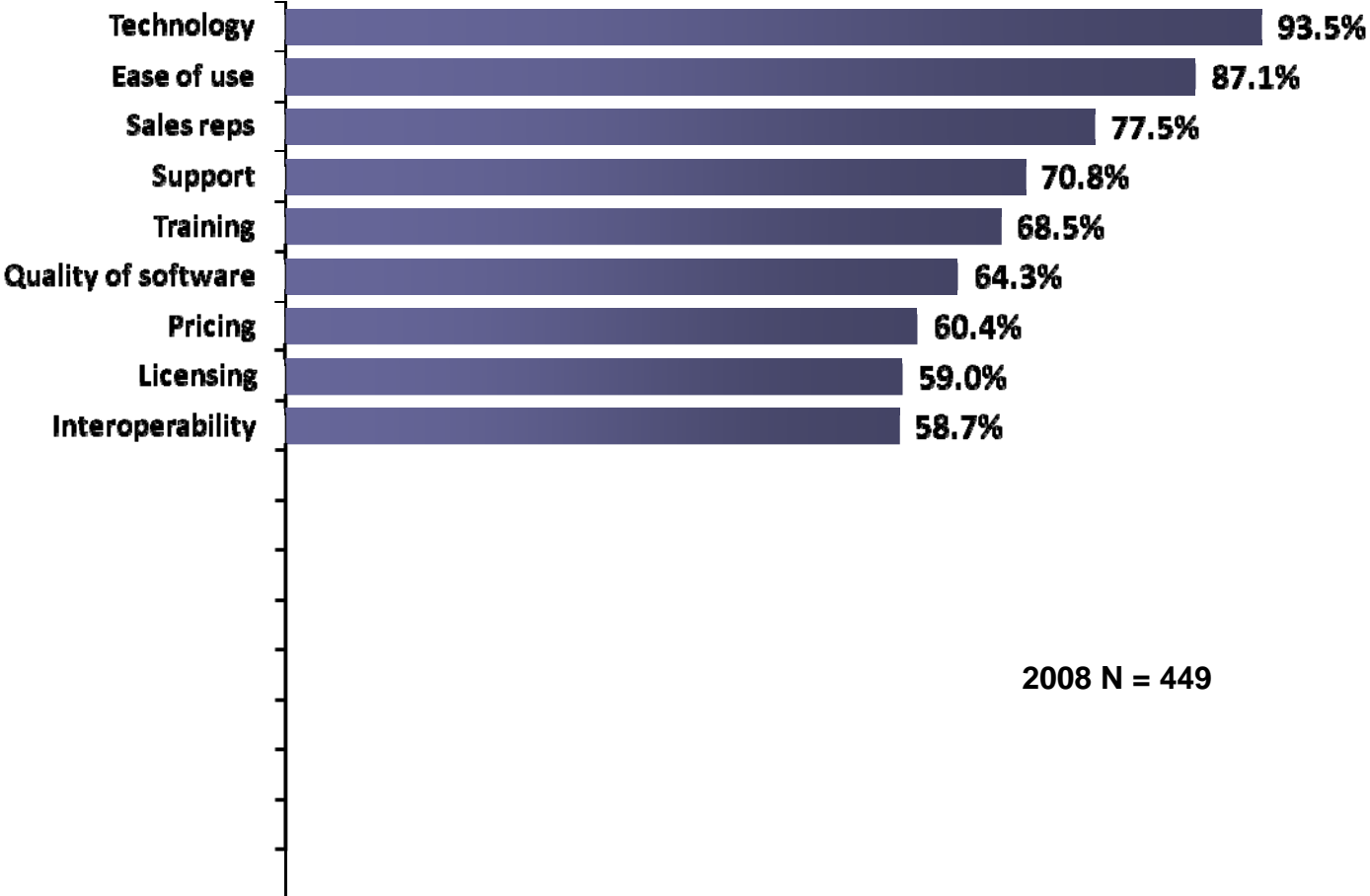
Will your organization be hiring more FPGA designers in the next year?



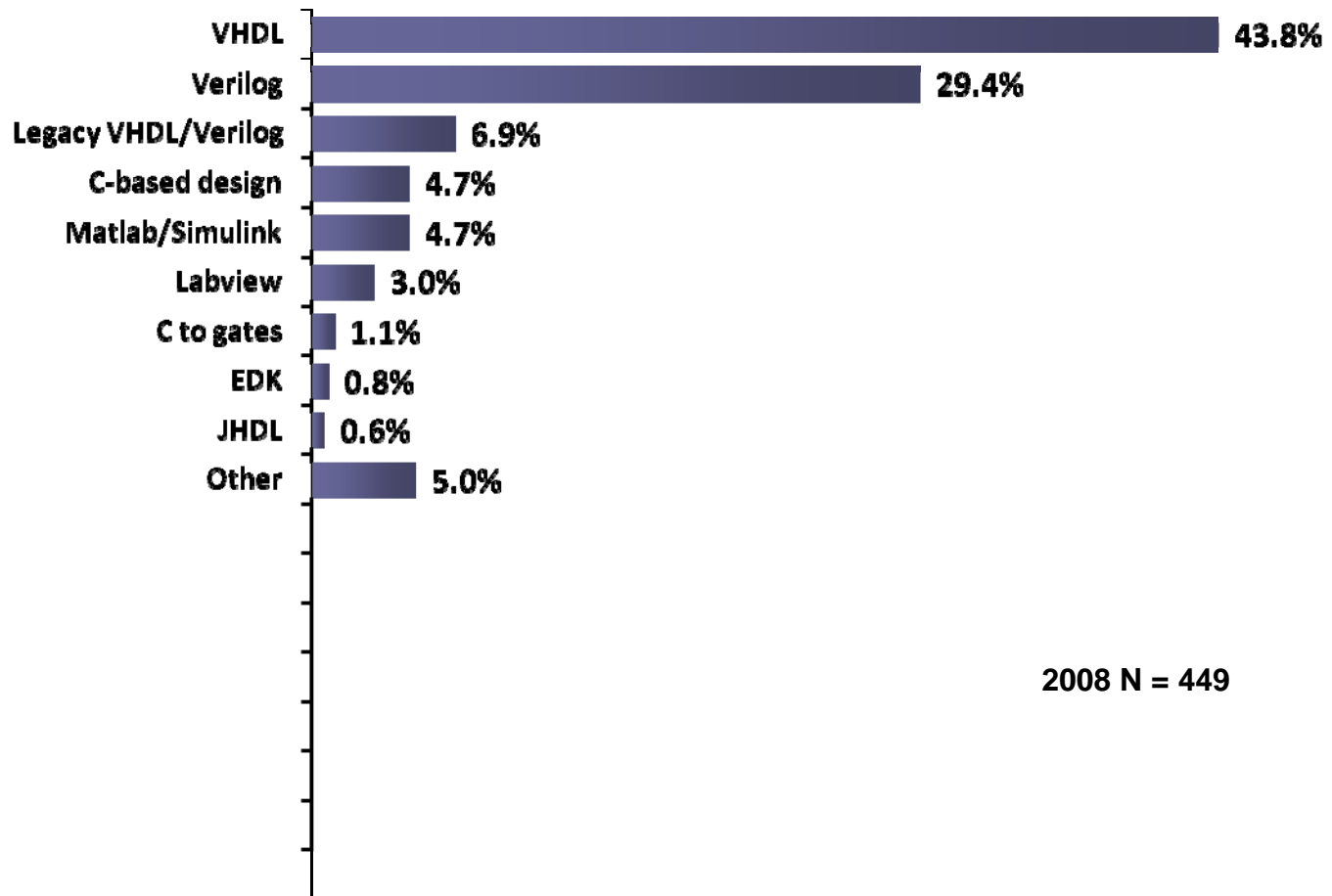
Are the managers in your organization also skilled FPGA designers?



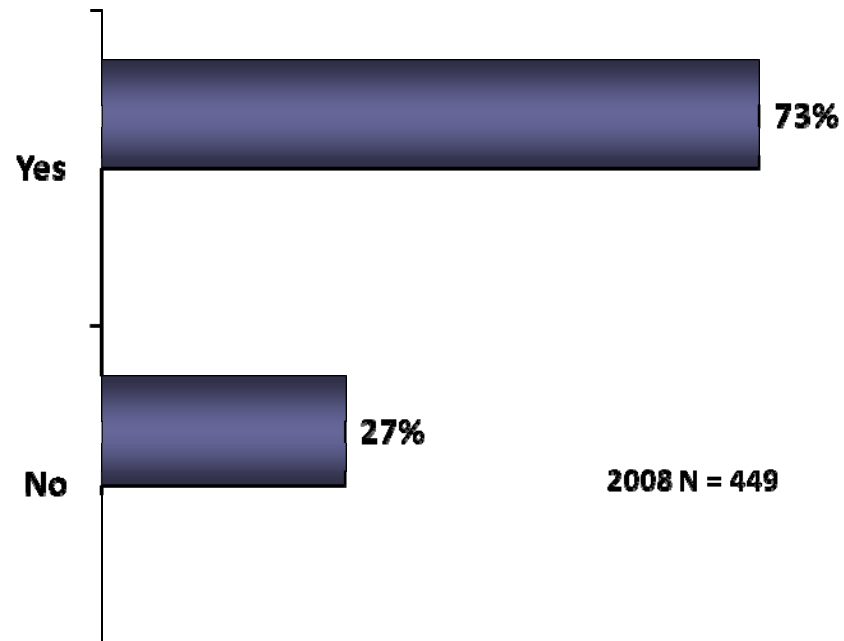
As you reflect on your total experience with FPGA vendors, what is your opinion or impression of the following? - *Favorable*



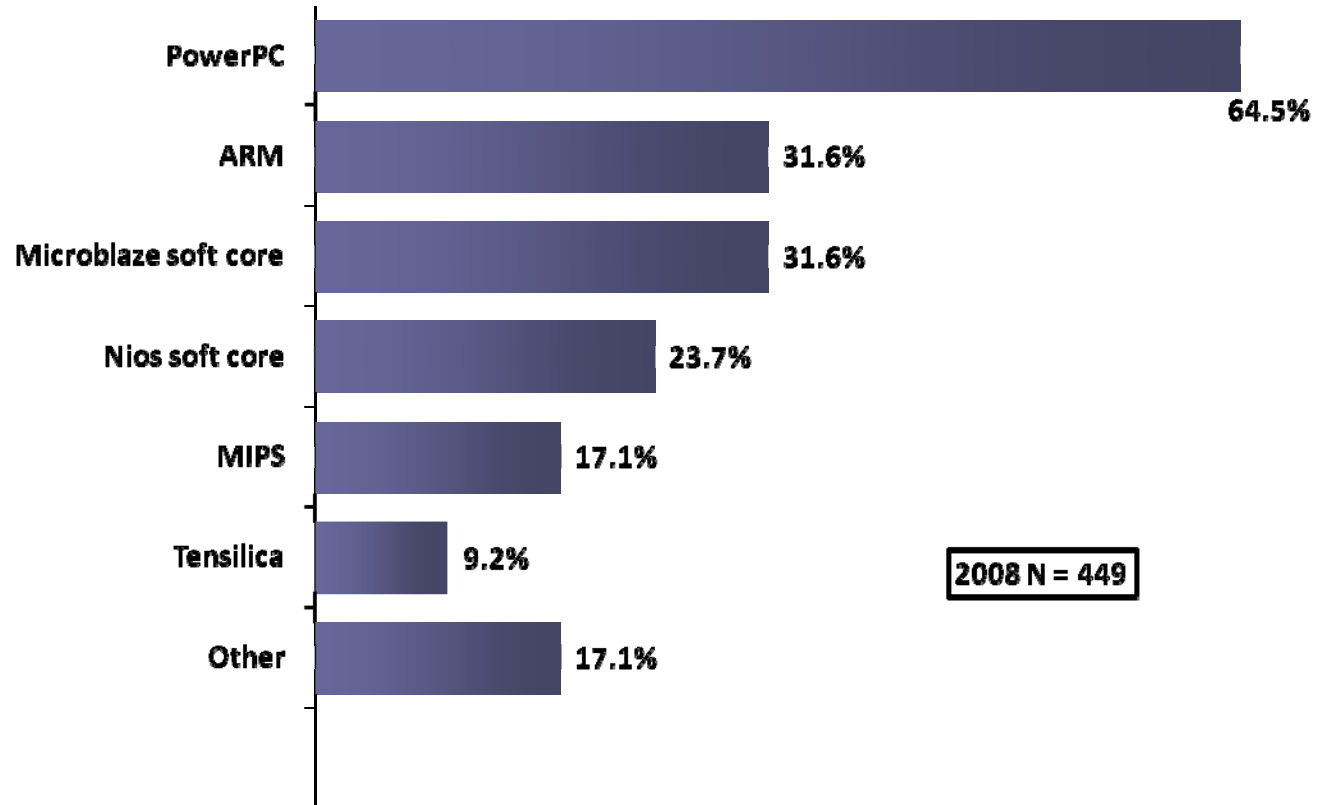
Choose your preferred method of design entry and languages used in your recent FPGA designs.



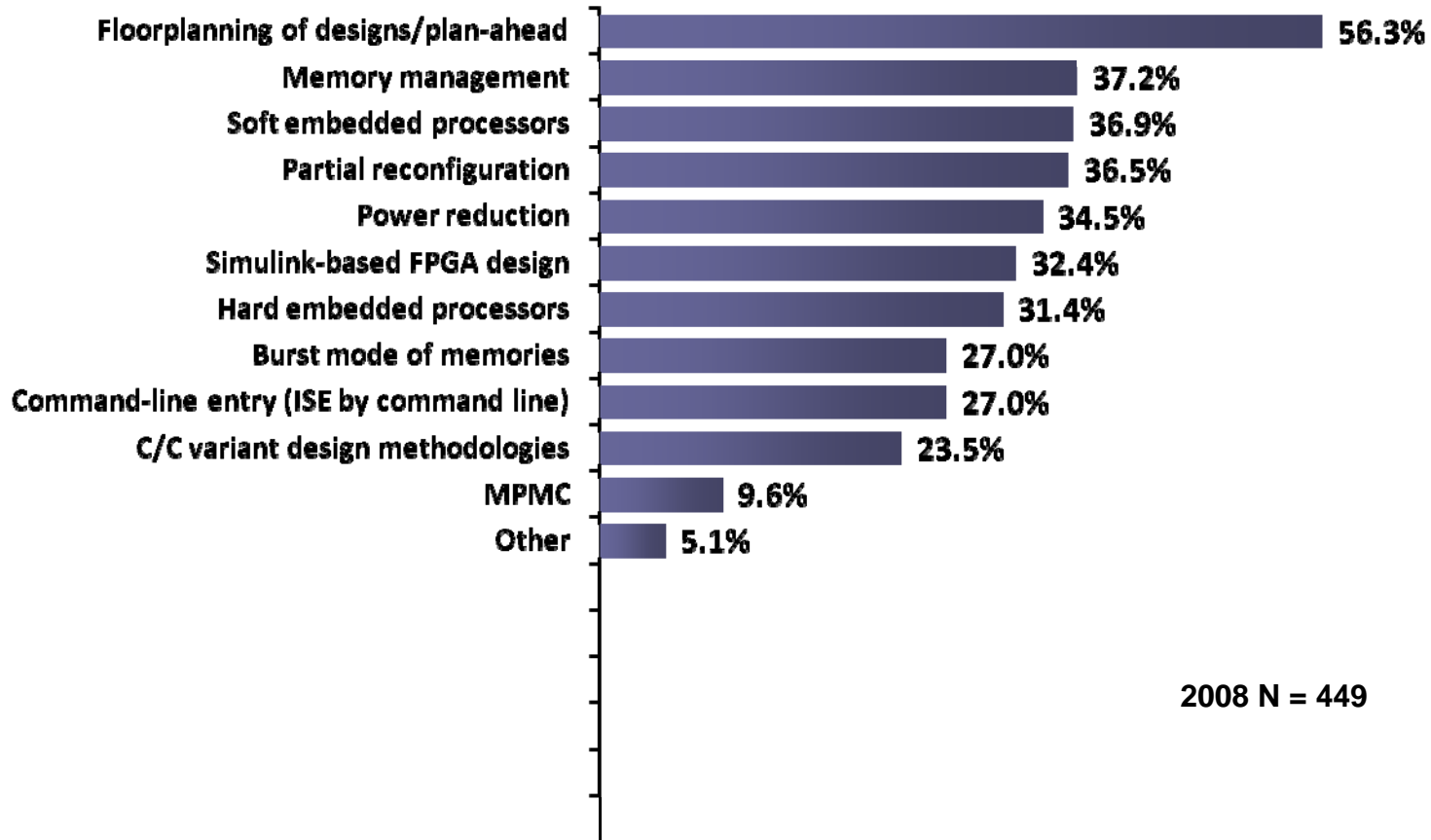
Does your project require software written for *embedded* processors?



Which of the following embedded processor cores do you currently use in your designs? (Multiple responses OK)



If you use Xilinx or Altera design tools, could you use tutorials, classes, how-to videos, etc. for any of these topics?



Write in Questions – How can FMAC help?

Q. What are the three biggest issues the FPGA Mission Assurance Center can solve to increase your organizations possibility of success with FPGA projects?

A1. Design obsolescence caused by IP updates/deletions from vendor libraries

A2. Provide tutorials on best practices in estimation, design, verification, documentation

A3. Find a way to easily get an embedded processor core certified to DO-254/DO-178 Level A

A4. Quit sending out 12-minute surveys that take an hour and a half.





FPGA Mission Assurance Center

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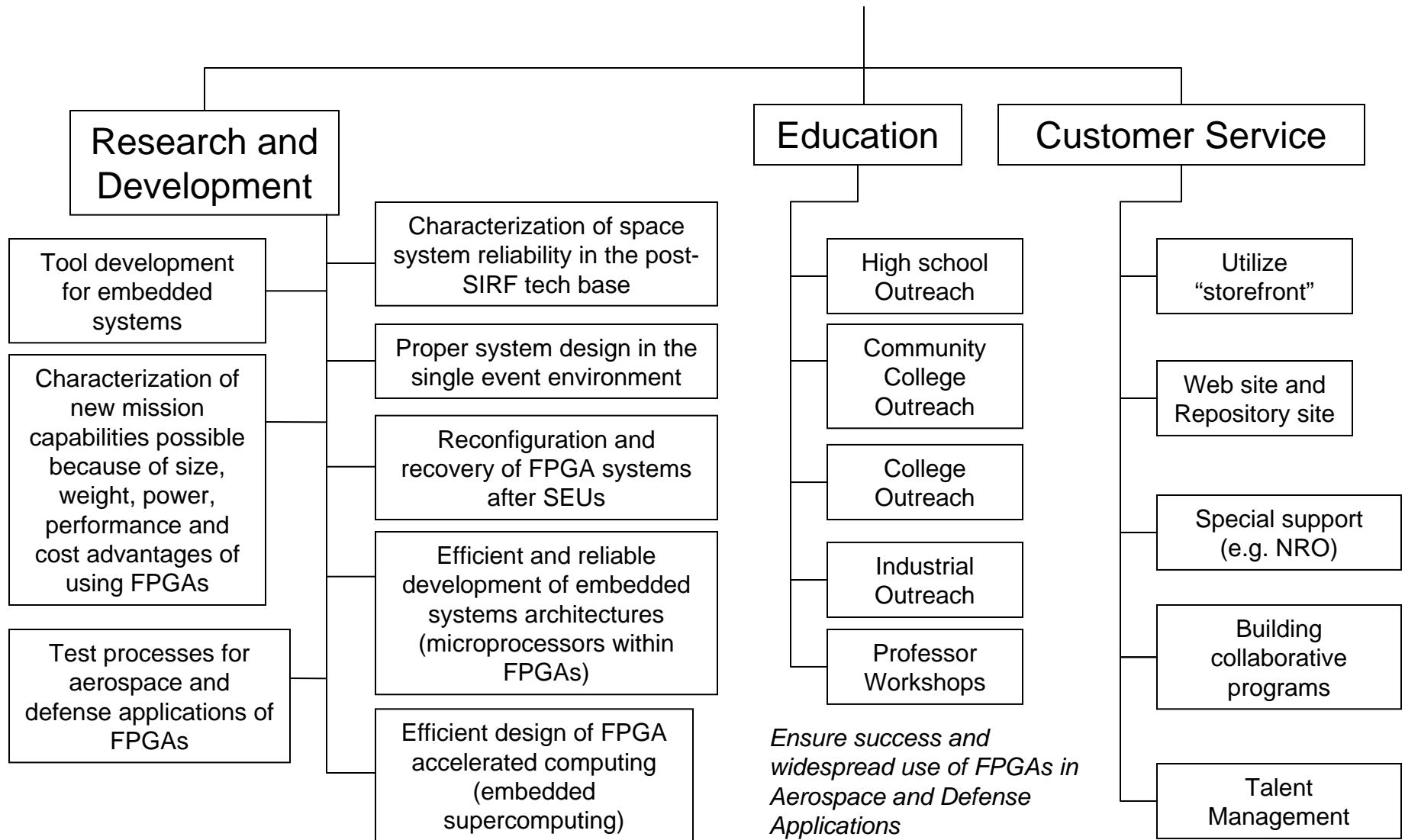
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Work Breakdown Structure



How We Do It: Education

University



Community College



K-12



Questions

